



IBM Haifa Verification Conference 2006 – Agenda

Day 2: Hardware Verification and Software Testing Tools

October 24, 2006

Tools for Modeling and Static Analysis Session chair: Avi Ziv, IBM Haifa Labs	13:30 Lunch
9:30 Smart-lint: Improving the Verification Flow <i>Itai Yarom and Viji Patil, Intel</i>	Tools for Dynamic Methods Session chair: Gadi Auerbach, IBM Haifa Labs
10:00 Model-Driven Development with the jABC <i>Bernhard Steffen, Tiziana Margaria, Ralf Nagel, Sven Joerges, and Christian Kubczak, University of Dortmund, Germany / University of Potsdam, Germany</i>	15:00 Invited speaker: Practical Methods in Coverage-oriented Verification of the Merom Microprocessor <i>Alon Gluska, Intel</i>
10:30 Detecting Design Flaws in UML State Charts for Embedded Software <i>Janees Elamkulam, Sandeep Kohli, Gururaja Kowlali, Satish Chandra Gupta, Sai Dattathrani, Ziv Glazberg, Ishai Rabinovitz, and Claudio Paniagua Macia, IBM</i>	15:45 Invited speaker: Increasing Predictability in the Design Verification Process <i>Andrew Piziali, Cadence</i>
11:00 Seqver: A Sequential Equivalence Verifier for Hardware Designs <i>Daher Kaiss, Silvian Goldenberg, Ziyad Hanna, and Zurab Khasidashvili, Intel</i>	16:30 An Open Source Simulation Model of Software Testing <i>Shmuel Ur, Elad Yom-Tov, and Paul Wernick, IBM</i>
11:30 Break	17:00 Conclusion
Panel Moderator: Shmuel Ur, IBM Haifa Labs	
12:00 Unpaved Road between Hardware Verification and Software Testing Techniques	